

FIG. 2

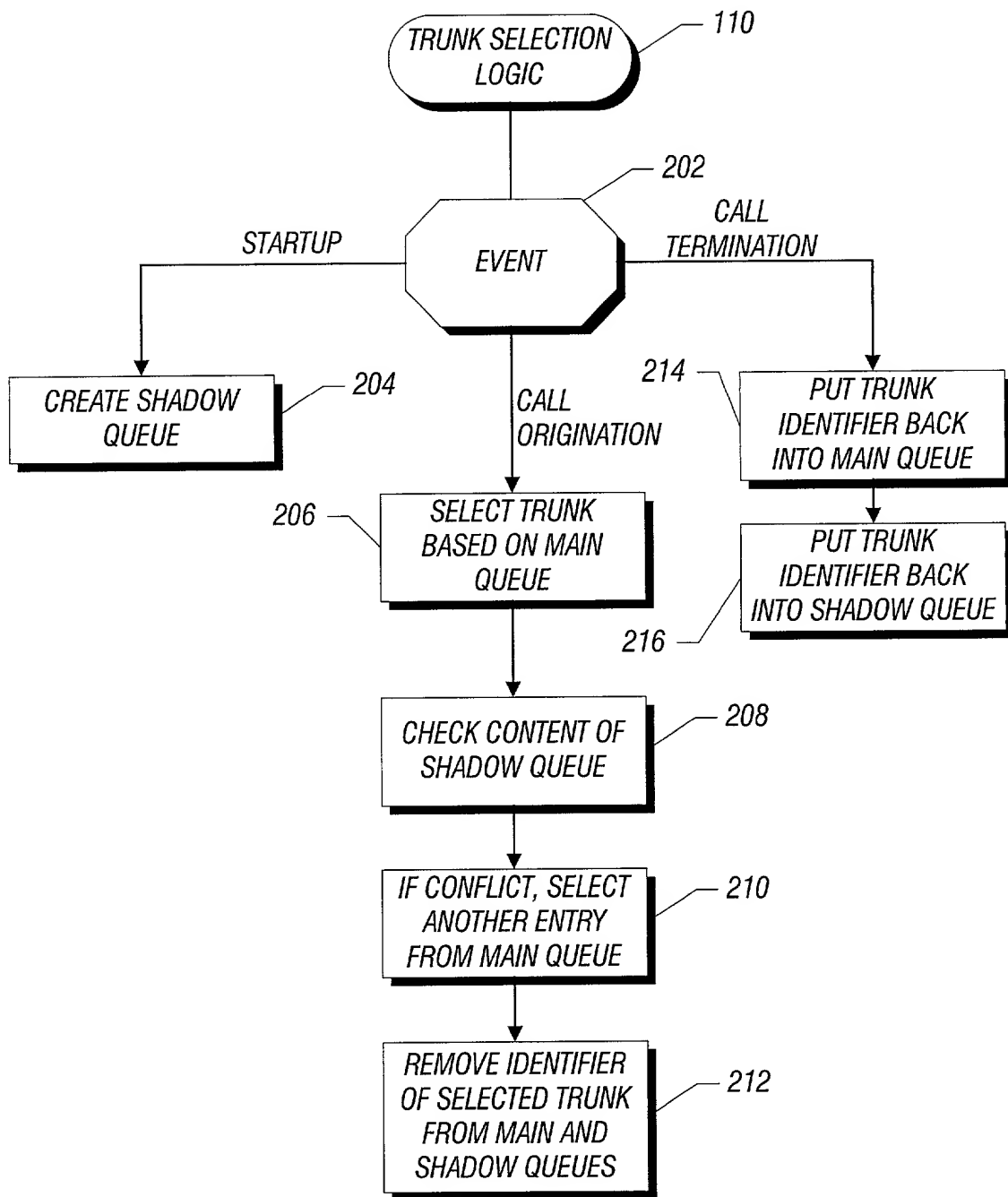
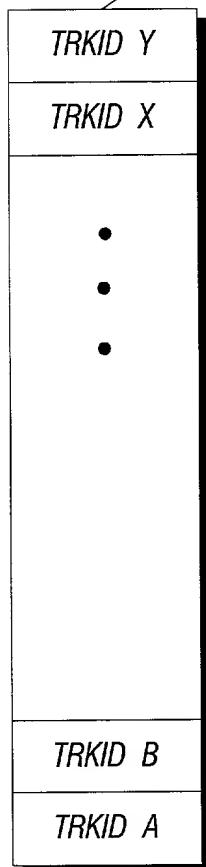


FIG. 3

SHADOW
QUEUE

114



MAIN
QUEUE

112

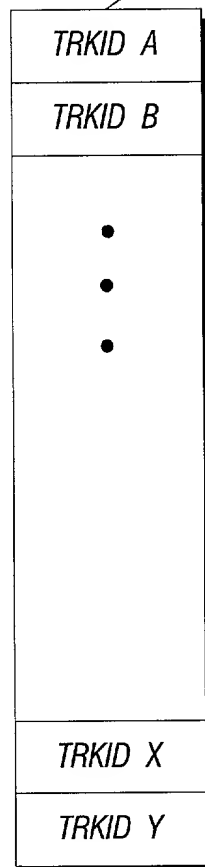


FIG. 4

FIG. 5 is a block diagram of a switch 300 in accordance with one embodiment of the present invention. The switch 300 includes a processing module 304, a messaging module 320, a switching bus 302, and a peripheral module 306. The processing module 304 includes a processor 308, storage 310, trunk selection logic 312, trunk selection queues 314, and call control logic 316. The messaging module 320 is connected to the processing module 304 and a signaling interface. The switching bus 302 is connected to the processing module 304 and the peripheral module 306. The peripheral module 306 is connected to the switching bus 302 and a switch 300.

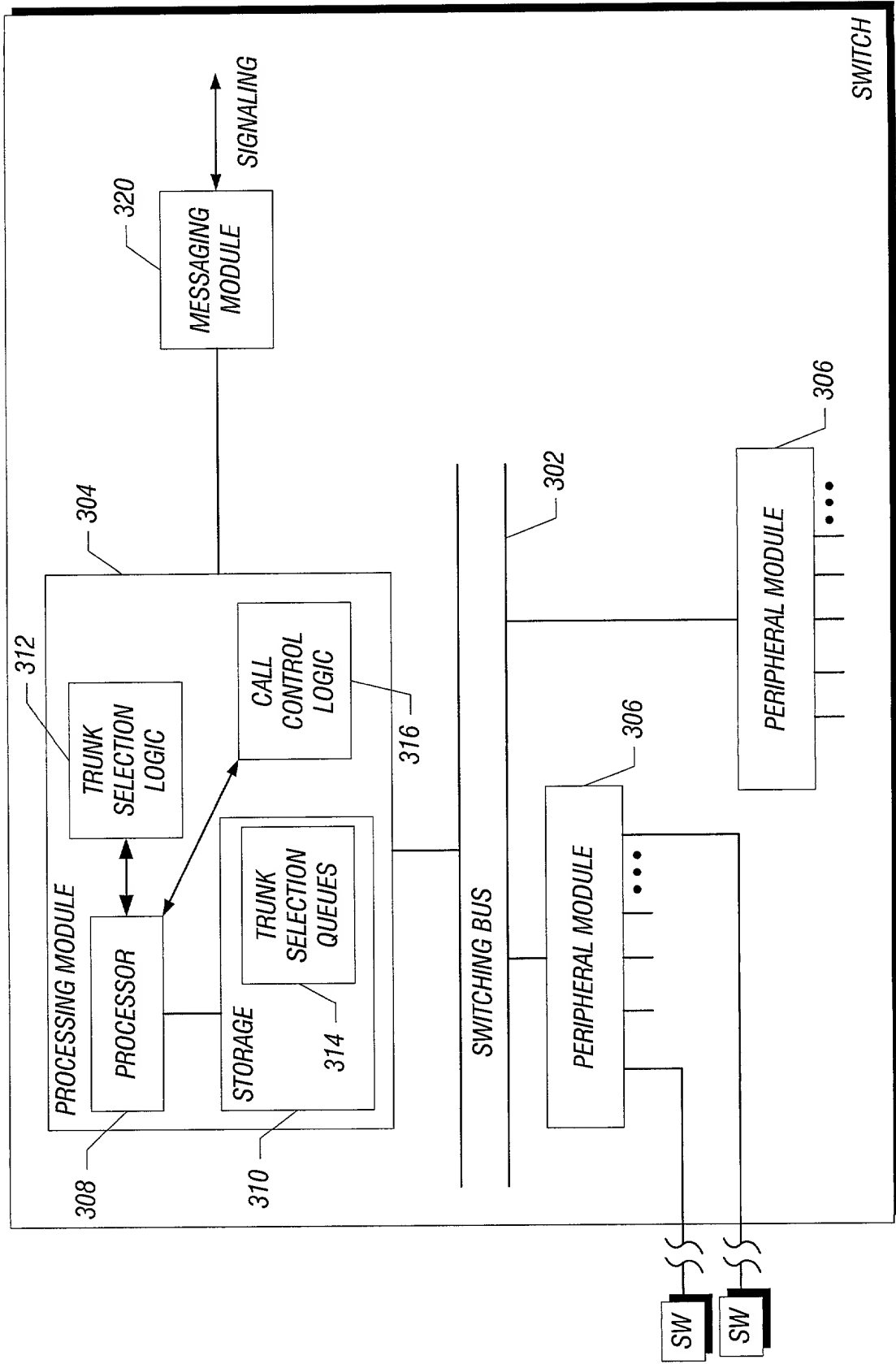


FIG. 5